

Notice of References Cited	Application/Control No. 10/749,052	Applicant(s)/Patent Under Reexamination BOLTON ET AL.	
	Examiner Eric B. Kiss	Art Unit 2192	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
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	V	Rajiv Gupta, et al., "Path Profile Guided Partial Dead Code Elimination Using Predication," 1997, proc. 1997 Int'l Conf. on Parallel Architectures and Compilation Techniques, pp. 102-113.
	W	Jens Knoop, et al, "Partial Dead Code Elimination," 1994, proc. ACM SIGPLAN 1994 conference on Programming language design and implementation, pp. 147-158.
	X	Munehiro Takimoto and Kenichi Harada, "Partial Dead Code Elimination Using Extended Value Graph," 1999, Springer-Verlag, Lecture Notes in Computer Science, vol. 1694, proc. Of the 6th Int'l Symposium on Static Analysis, pp. 179-193.

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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